

U.S.N.

B.M.S. College of Engineering, Bengaluru-560019

Autonomous Institute Affiliated to VTU

June / July 2024 Semester End Make-Up Examinations

Programme: B.E.

Branch: MD/EIE

Course Code: 23ES3PCAME

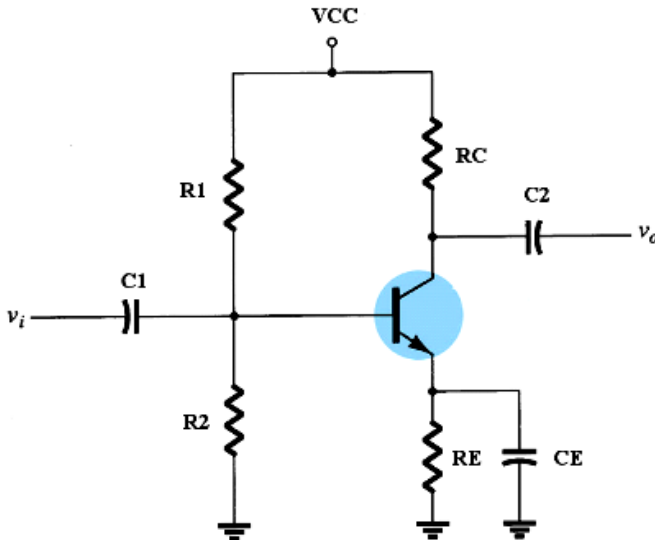
Course: Analog Microelectronics

Semester: III

Duration: 3 hrs.

Max Marks: 100

Instructions: 1. Answer any FIVE full questions, choosing one full question from each unit.
2. Missing data, if any, may be suitably assumed.

Important Note: Completing your answers, compulsorily draw diagonal cross lines on the remaining blank pages. Revealing of identification, appeal to evaluator will be treated as malpractice.			UNIT - I	<i>CO</i>	<i>PO</i>	Marks
	1	a)	What do you mean by clipping circuits? Draw and explain symmetrical double ended diode biased clipper circuit with the help of transfer characteristics.	<i>CO1</i>	<i>PO3</i>	08
		b)	Determine the voltage V_{CEQ} and I_{CQ} for the voltage divider configuration of the circuit shown in Figure 1.b) $R_1=39k\Omega$, $R_2=3.9k\Omega$, $R_C=10k\Omega$, $R_E=1.5k\Omega$, $C_1=10\mu F$, $C_2=10\mu F$, $C_E=50\mu F$, $\beta=140$ and $V_{CC}=22$ volts.	<i>CO1</i>	<i>PO3</i>	08
						
			Fig.1.b)			
		c)	Draw the circuit diagram of BJT re Transistor model.	<i>CO1</i>	<i>PO3</i>	04
		OR				
2	a)	Design a clamper circuit to perform the function indicated in Fig 2.a.	<i>CO1</i>	<i>PO3</i>	06	

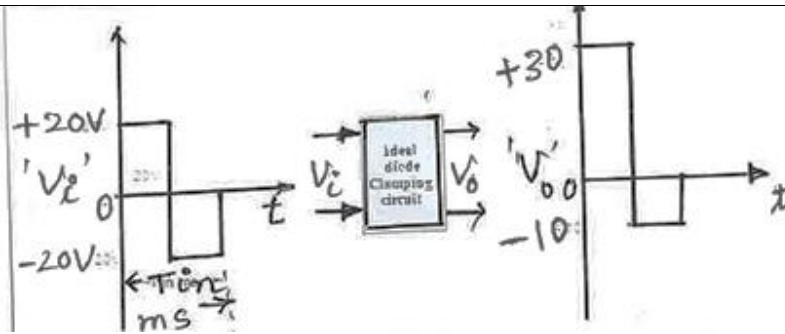


Fig.2.a.

- b) For the circuit shown in Fig.2.b. Determine the DC bias voltage V_{CE} and I_C for the voltage divider BJT circuit. Given $R_1=16k\Omega$, $R_2=9.2k\Omega$, $R_C=3.9k\Omega$, $R_E=680\Omega$, $V_{CC}=16$ volts and $\beta=80$.

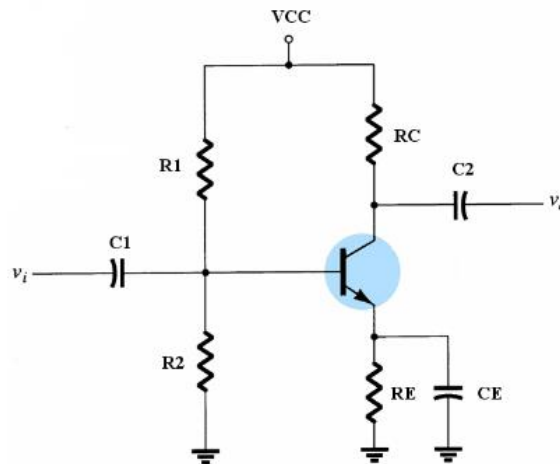


Fig.2.b.

- c) For the network of Figure 2.c., determine: i). Z_i , ii). Z_o , iii). A_v , iv). A_i

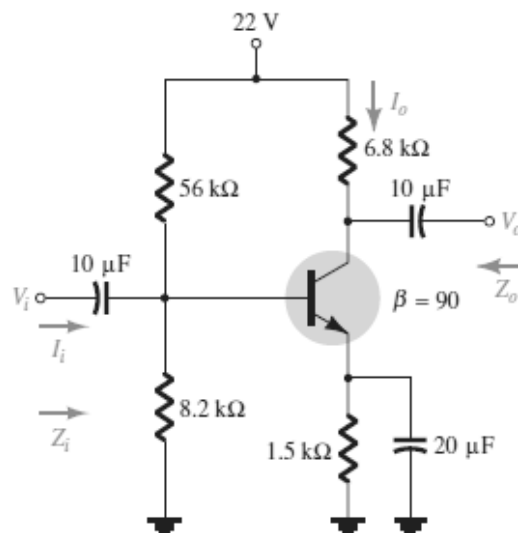
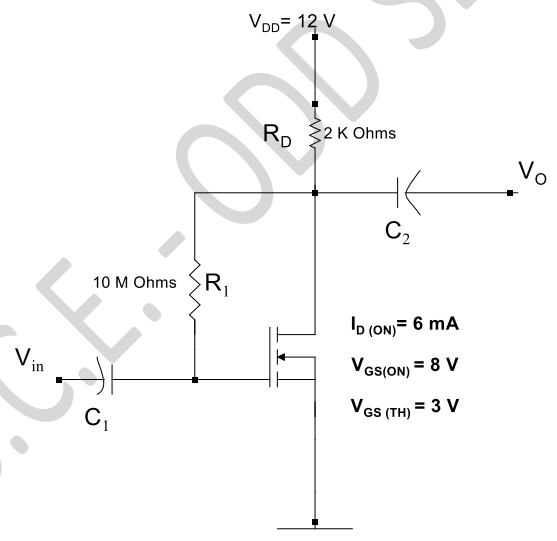


Fig. 2.c.

UNIT - II

- a) Discuss the factors that affects the low frequency response of a BJT- CE amplifier.

	b)	Prove that Miller effect capacitance, $C_{\text{Miller}} = (1-AV) \cdot C_f$.	CO2	PO3	06
	c)	Name four topologies of the feedback amplifiers. Draw the block schematic of voltage series feedback amplifier and also voltage series feedback Amplifier having gain $A = -100$, $R_{\text{in}} = 10 \text{ k}\Omega$ and $R_{\text{out}} = 20 \text{ k}\Omega$ for feedback, $\beta = -0.1$.	CO2	PO3	08
		UNIT - III			
4	a)	How are power amplifiers classified? and explain them in brief.	CO2	PO3	06
	b)	The class A transformer coupled power amplifier has zero signal collector current of 50 mA. If the collector supply voltage is 5 V, find (i) the maximum a.c. power output (ii) the power rating of transistor (iii) the maximum collector efficiency.	CO3	PO3	06
	c)	Derive expression for efficiency of Class B push-pull amplifier. For a class B Amplifier providing a 20V peak signal to a 16Ω load (speaker) and a power supply of $V_{\text{CC}} = 30\text{V}$. Determine the input power, output power and power efficiency.	CO4	PO4, PO5	08
		UNIT - IV			
5	a)	Discussing the Basic Structure of MOSFET, explain its principle of Operation of MOSFETS.	CO2	PO2	10
	b)	For the given circuit in Figure 5(b) $I_{\text{D(ON)}} = 6 \text{ mA}$, $V_{\text{GS(ON)}} = 8 \text{ V}$, $V_{\text{GS(TH)}} = 3 \text{ V}$. Calculate V_{GS} , I_{D} and V_{GS} .	CO3	PO3	10
		 <p>Fig. 5(b)</p>			
		UNIT - V			
6	a)	Draw the T Equivalent circuit model of MOS device and comment on the gate current value.	CO3	PO3	10
	b)	Figure 6.b. Shows the basic circuit of single-stage MOS amplifier. Consider for the case $V_{\text{DD}} = V_{\text{SS}} = 10 \text{ V}$, $I = 0.5 \text{ mA}$, $R_{\text{G}} = 4.7 \text{ M}\Omega$, $R_{\text{D}} = 15 \text{ k}\Omega$, $V_{\text{t}} = 1.5 \text{ V}$, and $k'_n = 1 \text{ mA/V}^2$. Find V_{OV} , V_{GS} , V_{S} and V_{D} . Also, Calculate the values of g_{m} , r_{o} , assuming the $V_{\text{A}} = 75 \text{ V}$. What is the maximum possible signal swing at the drain for which MOSFET remains in saturation.	CO4	PO4, 5	10

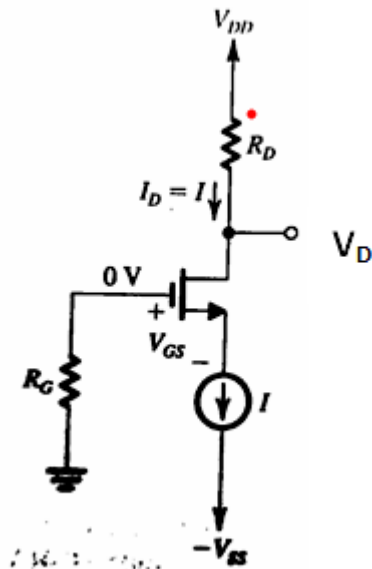


Fig 6.b.

OR

7	a)	For Small-signal operation of the enhancement MOSFET amplifier, derive $A_v = v_d/v_{gs} = -gm.R_D$.	CO2	PO2	06
	b)	Draw and explain FET based phase shift oscillator with circuit diagram.	CO5	PO2, PO3	08
	c)	Analyze the Wilson MOS mirror circuit with improved performance. Justify for the reducing β dependency and increase in output resistance in Wilson MOS mirror circuits.	CO5	PO2, PO3	06
